

(Date)

FILED WITH: DISK (CRF)

U.S. **UTILITY** Patent Application

SUBCLASS VINC

જજ

Manufacture method for semiconductor device with small variation in mos. threshold voltage

(Primary Examiner)

(Legal Instruments Examiner)

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

APPLICATION NO.

09/518709

of U.S Patent. No.

The terminal ____months of this patent have been disclaimed.

Jakahisa Yamaha

CONT/PRIOR

CLASS

432

ART.UNIT

PATENT DATE

EXAMINER-

PATIENT NUMBER

Date Paid

FICHE CD-ROM

ISSUE BATCH NUMBER

Amount Due